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/usr/share/doc/alliance-doc-5.0/tutorial/simulation/src

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```
> man <tool name>
```

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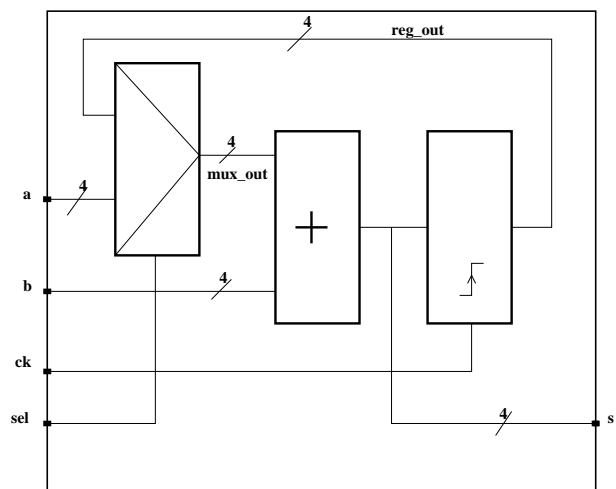
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```
> make
```

```
> make clean  
> make
```

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```
>  
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> >
```

```
> asimut -b -c <file name>
```

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```
> MBK_WORK_LIB = .  
> MBK_CATA_LIB = .  
> MBK_CATAL_NAME = CATAL  
> MBK_IN_LO = VST
```

```
> export var = value
```

```
> var = value  
> export var
```

```
> setenv var value
```

```
> vasy -Vao addaccu4.vhdl
```

```
> man 5 pat
```

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```
> asimut -b addaccu patterns result_vbe
```

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```
> asimut -b addaccu_dly patterns_dly result_dly
```

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```
> man genpat
```

```
> man AFFECT
```

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```
> genpat pat_new
```

```
> asimut -b -zerodelay addaccu pat_new res_new
```

```
> MBK_IN_LO = vst  
> export MBK_IN_LO
```

```
> asimut -c addaccu
```

```
> asimut -zerodelay -nores addaccu res_new
```

```
> MBK_CATA_LIB=$ALLIANCE_TOP/cells/sxlib  
> export MBK_CATA_LIB
```

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```

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```
> asimut -zerodelay -nores addaccu res_new
```

```
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```

```
> asimut addaccu pat_new res_dly
```

```
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```

```
> asimut addaccu pat_new res_dly
```
